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f	or FY 20	05	First Named Inventor	Rofougaran CE			ECENER
			Examiner Name	M. Milord			AL FAX CEAR
Applicant claim	is small entity stat	tus. See 37 CFR 1.27	Art Unit	2682	·	FER) 1
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Signature	Michael I.	<u>(</u>	Registration No		1		
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Attorney Docket No. 15258US08

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the	Application of:	Ś	Confirm	ation No.:	3857	RECEI CENTRAL FAX	
		A. Rofougaran et al.	Custome	er No. :	23446		2006
Serial	No:	09/698,498	· -			ŗ	
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Filed:		October 27, 2000)				
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Exam	iner:	M. Milord)				
Group	Art Unit:	2682)))	Michael 7.	- Cur nael P/C	niz	
		•)	Reg.	No. 44,6	636	

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Applicants respectfully enclose a PTO Form SB 08A that lists documents that were cited during the prosecution of the grandparent application and parent application to the present application. The grandparent application is United States Patent Application Serial No. 09/634,552, filed August 8, 2000. The parent application is United States Patent Application Serial No. 09/695,715, filed October 23, 2000.

The present application is a continuation of United States Patent Application Serial No. 09/695,715, filed October 23, 2000, which, in turn, is a continuation-in-part of United States Patent Application Serial No. 09/634,552, filed August 8, 2000.

09698498

Appl. No. 09/698,498

Supp. IDS dated Feb. 14, 2006

Under M.P.E.P. § 609.02 (Rev. 3, Aug. 2005), copies of the items listed on the accompanying PTO Form SB 08A need not be resubmitted in a continuing application. Therefore, no copies are enclosed.

The filing of a supplemental information disclosure statement shall not be construed to be an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

An information disclosure statement fee of \$180 is believed to be due.

It is respectfully requested that this paper and enclosures be considered by the Examiner. Furthermore, the Examiner is respectfully requested to initial a copy of the enclosed PTO Form SB08A and return an initialed copy to Applicants to indicate consideration of the enclosed listed documents.

The Commissioner is hereby authorized to charge additional fees or credit overpayments to the deposit account of McAndrews, Held & Malloy, Account No. 13-0017.

Date: February 14. 2006

Respectfully submitted,

Michael T. Cruz

Reg. No. 44,636

McAndrews, Held & Malloy, Ltd. 500 West Madison Street, 34th Floor

Chicago, Illinois 60661

Telephone: (312) 775-8084

Fax: (312) 775-8100

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Substitute for form 1449A/PTO	Complete If Known			
	Application Number	09/698,498		
SUPPLEMENTAL INFORMATION	Filing Date	October 27, 2000		
DISCLOSURE STATEMENT BY APPLICANTS	First Named Inventor	A. Rofougaran		
DIOOCOOKE STATEMENT BY AFFEIGANTS	Group Art Unit	2682		
tura an manu abaut	Examiner Name	M. Milord		
(use as many sheets as necessary) Sheet 1 of 2	Attorney Docket Number	15258US08		

			U.S. PATENT DO	CUMENTS	
Examiner Initial*	Cite No.1	Document Number Number-Kind Code * (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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		6,738,601	05.2004	Rofougaran et al.	

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Substitute for	r form 1449A/PTO		Complete if Known			
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	FOREIGN PATENT DOCUMENTS						
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		EP 0 803 997 A2	10.1997	Kuiri, Tapio			

·		OTHER ART - NON PATENT LITERATURE DOCUMENTS
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published
		A. Rofougaran, "A Single-Chip Spread-Spectrum Wireless Transceiver in CMOS," Final Report, Integrated Circuits & Systems Laboratory Electrical Engineering Department, University of California, Los Angeles, California, 339 pages (1999).
		J. Lee et al., "Design of Spiral Inductors on Silicon Substrates with a Fast Simulator", 4 pages, Electrical Engineering Department, University of California, Los Angeles, California, presented at the European Solid-State Circuits Conference, The Hague, The Netherlands (September 1998).
		P.Y. Chan, "A Highly Linear 1-GHz CMOS Downconversion Mixer," Integrated Circuits & Systems Laboratory, University of California, Los Angeles, California, presented at the European Solid State Circuits Conference, Seville, Spain, 4 pages (September 22-24, 1993).

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Date	February 14, 20						
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